

# TEST REPORT IEC 62116

### Test procedure of islanding prevention measures for utilityinterconnected photovoltaic inverters

Report Number. ...... 2219 / 0190-2-M1

Date of issue .....: 17/10/2019

Total number of pages...... 12

Name of Testing Laboratory

preparing the Report...... SGS Tecnos, S.A. (Electrical Testing Laboratory)

Applicant's name...... EVOLVE ENERGY GROUP CO., LIMITED

Address ...... RM 702, 7/F FU FAI COMM CTR 27 HILLIER ST SHEUNG

WAN, HK

Test specification:

Standard .....: IEC/EN 62116: 2014 (Second Edition)

Test procedure .....: Characteristic Examination

Non-standard test method .....: N/A

Test Report Form No. .....: IEC62116B

Test Report Form(s) Originator ....: TÜV SÜD Product Service GmbH

Master TRF...... Dated 2014-10

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Test item description:	Solar Grid-tied Inverter		
Trade Mark:	EVVO		
Manufacturer:	EVOLVE ENERGY GROUP CO., LIMITED		
Model/Type reference.:	EVVO 15000TLG23P		
Ratings::	DC input: 160V-960V Max.21A /11 A		
	<b>AC output</b> : 3/N/PE 230/400Va.c, 50Hz, Max.3 x 24A, 15000W		
	Serial Number: SN1CS015K3G061		
	Firmware version: V0.21		

Respo	nsible Testing Laboratory (as applicable	), testing procedure ar	nd testing location(s):
	CB Testing Laboratory:		
Testing	g location/ address:		
$\Box$	Associated CB Testing Laboratory:		
	Testing procedure: TMP/CTF Stage 1:	Shenzhen SOFAR SO	LAR Co., Ltd.
Testing location/ address:		XingDong Community,	gDa Industrial Park, District 68, XinAn Street, BaoAn District, dong Province, P.R. China
Tested	by (name, function, signature):	Hugo zhang (Project Engineer)	11 ufo 2 hang
		Roger Hu (Project Engineer)	Rogerba
Approv	ved by (name, function, signature:	Jacobo Tevar	
		(Technical Reviewer)	
$\Box$	Testing procedure: WMT/CTF Stage 2:		
	Testing procedure: SMT/CTF Stage 3 or 4:		



#### List of Attachments (including a total number of pages in each attachment):

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	50 Hz	
Attachment #	Description	Pages
Attachment I	Pictures of the EUT and Electrical Schemes	12pages
Attachment II	Graphics of the Test Results	3 pages
Attachment III	Graphics of the Islanding Behavior Detection	22 pages
Attachment IV	Testing Information	9 pages

#### Summary of testing:

## Tests performed (name of test and test clause):

All clauses except:

Sub-clause d) of the Table 5 of the point 6.1.
 Voltage and frequency trips shall be adjusted according to National Standards and/or local codes.

From the result of inspection and tests performed on the submitted sample we conclude that it complies with the requirements of the Standard

**Remarks:** All the test results are from the report below:

- IEC/EN 62116: 2014 (Second Edition)

Test Report No: 2219 / 0190-2 which issued by SGS Tecnos, S.A. (Electrical Testing Laboratory) on19/06/2019

#### Testing location:

Shenzhen SOFAR SOLAR Co., Ltd.

401, Building 4, AnTongDa Industrial Park, District 68, XingDong Community, XinAn Street, BaoAn District, Shenzhen City, Guangdong Province, P.R. China

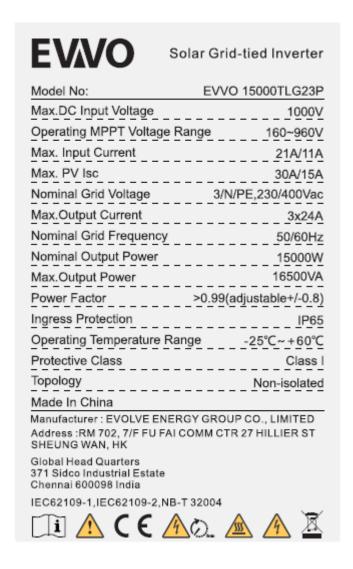
(All clauses)

#### **Summary of compliance with National Differences:**

No National Differences are addressed to this test report



#### Copy of marking plate(representative):



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#### Note:

- The above markings are the minimum requirements required by the safety standard. For the final production samples, the additional markings which do not give rise to misunderstanding may be added.
- 2. Label is attached on the side surface of enclosure and visible after installation
- Labels of other models are as the same with EVVO 15000TLG23P's except the parameters of rating.



Test item particulars:	Solar Grid-tied Inverter (Three Phase Inverter)	
Classification of installation and use:	Fixed (permanent connection)	
Supply Connection::	DC; PV	
:	AC; Grid connection	
Possible test case verdicts:		
- test case does not apply to the test object:	N/A	
- test object does meet the requirement:	P (Pass)	
- test object does not meet the requirement:	F (Fail)	
Testing:	CTF Stage 1 procedure	
Date of receipt of test item:	N/A	
Date (s) of performance of tests:	From 18/06/2019	
Canaral ramarka		
General remarks:		
"(See Enclosure #)" refers to additional information ap "(See appended table)" refers to a table appended to the		
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Throughout this report a ☐ comma / ☒ point is us	sed as the decimal separator.	
Manufacturer's Declaration per sub-clause 4.2.5 of l	ECEE 02:	
The application for obtaining a CB Test Certificate includes more than one factory location and a declaration from the Manufacturer stating that the sample(s) submitted for evaluation is (are) representative of the products from each factory has been provided		
When differences exist; they shall be identified in the	ne General product information section.	
Name and address of factory (ies):	Dongguan SOFAR SOLAR Co.,Ltd.  1F - 6F, Building E, No. 1 JinQi Road, Bihu Industrial Park, Wulian Village, Fenggang Town, Dongguan City, Guangdong Province,P.R. China.	



#### General product information:

Product covered by this report is grid-connected PV inverter for indoor or outdoor installation. The connection to the DC input and AC output are through connectors.

The Solar inverter converts DC voltage into AC voltage.

The input and output are protected by varistors to Earth. The unit is providing EMC filtering at the output toward mains. The unit does not provide galvanic separation from input to output (transformerless). The output is switched off redundant by the high power switching bridge and a two relays. This assures that the opening of the output circuit can operate in case of one error.

#### **Equipment Under Testing:**

EVVO 15000TLG23P

#### Variant models:

- EVVO 12000TLG23P
- EVVO 10000TLG23P

Model Number	EVVO 15000TLG23P	EVVO 12000TLG23P	EVVO 10000TLG23P		
Max. input voltage	1000Vd.c.				
Max. input current		21A/11A			
Operating MPPT voltage range		160V-960V			
Rated voltage		600V			
Full load DC Voltage Range	500V-850V	500V-850V	350V-850V		
Rated grid voltage	3/N/PE 230/400Va.c				
Rated grid frequency		50Hz			
Rated output power	15000W	12000W	10000W		
Max. output current	3 x 24A	3 x 20A	3 x 16.5A		
Power factor	0.8 leading to 0.8 lagging				
Ambient temperature	-25 °C ~60 °C				
Ingress protection	IP65				
Protective class		Class I			

The variants models have been included in this test report without tests because the following features don't change regarding to the tested model:

- Same connection system and hardware topology
- Same control algorithm.
- Output power within 2.5 and 2/3 of the EUT or Modular inverters.
- Same Firmware Version



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Clause	Requirement + Test	Result - Remark	Verdict

4	Testing circuit		
	The testing circuit shown in Figure 1 is employed.		Р
	Similar circuits are used for three-phase output.		Р
	Parameters to be measured are shown in Table 1		Р
	and Figure 1. Parameters to be recorded in the test		
	report are discussed in Clause 7.		
5	Testing equipment		•
5.1	Measuring instruments		Р
	The waveform measurement/capture device is able to record the waveform from the beginning of the islanding test until the EUT ceases to energize the island.	Oscillograph and Power analyzer equipped with memory function  Waveform caught from the	P
		switch open and the EUT	
	For multi-phase EUT, all phases are monitored.	cease to energize	P
	A waveform monitor designed to detect and	See Annex IV for testing	P
	calculate the run-on time may be used.	equipment information	[
	For multi-phase EUT, the test and measurement	cquipment imonnation	P
	equipment is recorded each phase current and each phase-to-neutral or phase-to-phase voltage, as appropriate, to determine fundamental frequency active and reactive power flow over the duration of the test.		'
	A sampling rate of 10 kHz or higher is recommended. The minimum measurement accuracy is 1 % or less of rated EUT nominal output voltage and 1 % or less of rated EUT output current	Less than 1% of the rated EUT output current	Р
	Current, active power, and reactive power measurements through switch S1 used to determine the circuit balance conditions report the fundamental (50 Hz or 60 Hz) component.		Р
5.2	DC power source		
5.2.1	General		Р
	A PV array or PV array simulator (preferred) may be used. If the EUT can operate in utility-interconnected mode from a storage battery, a DC power source may be used in lieu of a battery as long as the DC power source is not the limiting device as far as the maximum EUT input current is concerned.	Chroma PV simulator used	P
	The DC power source provides voltage and current necessary to meet the testing requirements described in Clause 6.		Р
5.2.2	PV array simulator		Р
V:4:4	The tests are conducted at the input voltage defined in Table 2 below, and the current is limited to 1,5 times the rated photovoltaic input current, except when specified otherwise by the test requirements.		P
	A PV array simulator is recommended, however, any type of power source may be used if it does not influence the test results.		Р
5.2.3	Current and voltage limited DC power supply with series resistance		N/A



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	A DC power source used capable of EUT maximum achieve EUT maximum or and maximum EUT input or	utput power) at minimum		N/A
	The power source provide voltage limit, set to provide current and open circuit vothe series and shunt resist	e the desired short circuit oltage when combined with		N/A
	A series resistance (and, oresistance) is selected to puthe range: Output power: Sufficient to output power and other lesconditions of table 5. Response speed: The resto a step in output voltage change, results in a settlin within 10% of its final value Stability: Excluding the value Stability: Excluding the value EUT MPPT, simulator out within 2% of specified power factors of the test: from the point of achieved until the island callowable run-on time is expower factor: 0.25 to 0.8	provide a fill factor within provide maximum EUT vels specified by test  ponse time of a simulator , due to a 5% load g of the output current to e in less than 1ms. riations caused by the put power remains stable wer level over the duration where load balance is ondition is cleared or the		N/A
5.2.4	PV array			N/A
				N/A
	Testing is limited to times	when the irradiance varies the duration of the test as e pyranometer or be necessary to adjust the eve the input voltage and		N/A
5.3	AC power source			
	The utility grid or other AC used as long as it meets the Table 4.		AC power source used meets the conditions specified	Р
5.4	Voltage Voltage THD Frequency Phase angle distance 1) 1) Three-phase case only	Conditions  Nominal ±2.0 %  < 2.5 %  Nominal ±0.1 Hz  120 ° ± 1.5 °		
5.4	AC loads			



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Clause	Requirement + Test	Result - Remark	Verdict
	On the AC side of the EUT, variable resistance, capacitance, and inductance are connected in parallel as loads between the EUT and the AC power source. Other sources of load, such as electronic loads, may be used if it can be shown that the source does not cause results that are different than would be obtained with passive resistors, inductors, and capacitors.	Passive loads (variable resistance, capacitance and inductance) have been connected.	Р
	All AC loads are rated for and adjustable to all test conditions. The equations for Qf are based upon an ideal parallel RLC circuit. For this reason, non-inductive resistors, low loss (high Qf) inductors, and capacitors with low effective series resistance and effective series inductance are utilized in the test circuit. Iron core inductors, if used, are not exceed a current THD of 2 % when operated at nominal voltage. Load components are conservatively rated for the voltage and power levels expected. Resistor power ratings are chosen so as to minimize thermally-induced drift in esistance values during the course of the test.		Р
	Active and reactive power is calculated (using the measurements provided in Table 1) in each of the R, L and C legs of the load so that these parasitic parameters (and parasitics introduced by variacs or autotransformers) are properly accounted for when calculating Qf.		Р
6	Test for single or multi-phase inverter		
6.1	Test procedure	(see appended table)	Р
	The test uses an RLC load, resonant at the EUT nominal frequency (50 Hz or 60 Hz) and matched to the EUT output power.		Р
	For multi-phase EUT, the load is balanced across all phases and the switch S1 as in Figure 1 opens all phases		Р
	This test is performed with the EUT conditions as in Table 5, where power and voltage values are given as a percent of EUT full output rating.		Р
	a)Determine EUT test output power		Р
	b) .Adjusting the DC input source		P
	c)Turn off the EUT and open S1		P
	d) .Adjust the RLC circuit to have Qf = 1.0 ±0.05		P
	e)Connect the RLC load configured in step d) to		P
	the EUT by closing S2		
	f)Open the utility-disconnect switch S1 to initiate the test, Run-on time is recorded.		Р
	g)For test condition A, adjust the real load and only one of the reactive load components to each of the load imbalance conditions shown in the shaded portion of table 6. If any of the recorded run-on times are longer than the one recorded for the rated balance condition, then the non-shaded parameter combinations also require testing.		Р



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Clause	lause Requirement + Test Result - Remark Verdict					
		T				
	h) For test condition B and C, adjust the only one reactive load components by approximately 1,0% per test, within a total range of 95% to 105% of the operating point. If run-on times are still increasing at the 95% or 105% points, additional 1% increments have to be taken until run-on times begin decreasing.		P			
6.2	Pass/fail criteria					
	An EUT is considered to comply with the requirements for islanding protection when each case of recorded run-on time is less than 2 s or meets the requirements of local codes.	Run-on time is less than 2s in any case	Р			
7	Documentation					
	At a minimum, the following information is recorded and maintained in the test report.		Р			
	a) Specifications of EUT. Table 8 provides an example of the type of information that is provided.		Р			
	b) Measurement results. Table 9 provides an example of the type of information that is provided. Actual measured values is to be recorded.		Р			
	c) Block diagram of test circuit.		Р			
	d) Specifications of the test and measurement equipment. Table 10 provides an example of the type of information that is provided.		Р			
	e) Any test configuration or procedure details such as methods of achieving specified load and EUT output conditions.		Р			
	f) Any additional information required by the testing laboratory's accreditation.		Р			
	g) Specify the evaluation criterion from clause 6.2 that was utilized to determine if the product passed or failed the test.		Р			
Annex A	Islanding as it applies to PV systems(Informative)					
A.1	General					
A.2	Impact of distortion on islanding					
Annex B	Test for independent islanding detection device (relay	/)(Informative)				
B.1	Introduction					
B.2	Testing circuit					
B.3	Testing equipment					
B.4	Testing procedure					
B.5	Documentation					







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		IEC 62116		
Clause	Requirement + Test		Result - Remark	Verdict

6.1	Table: tested condition and run-on time	Р
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No.	PEUT (% of EUT rating)	Reactiv e load (% of normial)	Pac	Qac	Run-on time(ms)	P <sub>EUT</sub> (W)	Actual Q <sub>f</sub>	V <sub>DC</sub> (d.c.V)	Which load is selected to be adjusted (R or L)
	Test condtion A								
1	100	100	0	0	390	15015	1.00	802.9	
2	100	100	-5	-5	190	15010	0.98	802.2	R/L
3	100	100	-5	0	328	15021	0.98	803.6	R
4	100	100	-5	+5	326	15011	1.05	801.9	R/L
5	100	100	0	-5	304	15010	1.00	802.3	L
6	100	100	0	+5	324	15008	1.01	802.7	L
7	100	100	+5	-5	175	15012	1.01	803.2	R/L
8	100	100	+5	0	336	15016	0.98	802.5	R
9	100	100	+5	+5	366	15013	0.97	802.6	R/L
10	100	100	-10	+10		-	-		R/L
11	100	100	-5	+10					R/L
12	100	100	0	+10					L
13	100	100	+10	+10		-	-		R/L
14	100	100	+10	+5		-	-		R/L
15	100	100	+10	0					R
16	100	100	+10	-5		-	-		R/L
17	100	100	+10	-10		-	-		R/L
18	100	100	+5	-10					R/L
19	100	100	+5	10					R/L
20	100	100	0	-10					L
21	100	100	-5	-10					R/L
22	100	100	-10	-10					R/L
23	100	100	-10	-5					R/L
24	100	100	-10	0					R/L
25	100	100	-10	+5					R/L



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Test condtion B									
10	66	66	0	0	358	9915	1.00	566.7	
11	66	66	0	-5	133	9921	1.03	563.2	L
12	66	66	0	-4	172	9923	1.03	564.2	L
13	66	66	0	-3	141	9918	1.02	565.6	L
14	66	66	0	-2	278	9917	1.02	562.8	L
15	66	66	0	-1	350	9926	1.01	563.5	L
16	66	66	0	1	194	9932	0.99	563.8	L
17	66	66	0	2	350	9924	0.99	563.5	L
18	66	66	0	3	176	9922	0.99	563.6	L
19	66	66	0	4	196	9918	0.98	562.9	L
20	66	66	0	5	100	9925	0.98	564.1	L
21	66	66	0	6					L
				Test co	ndition C				
22	33	33	0	0	418	4964	1.00	306.3	
23	33	33	0	-5	177	4966	1.03	304.6	L
24	33	33	0	-4	374	4968	1.04	305.2	L
25	33	33	0	-3	282	4957	1.02	306.8	L
26	33	33	0	-2	328	4953	1.02	304.3	L
27	33	33	0	-1	144	4965	1.00	306.4	L
28	33	33	0	1	243	4961	1.00	305.7	L
29	33	33	0	2	185	4962	1.00	303.4	L
30	33	33	0	3	140	4959	0.99	305.6	L
31	33	33	0	4	172	4958	0.99	304.6	L
32	33	33	0	5	79	4960	0.98	304.2	L
33	33	33	0	6					L

#### Remark:

For test condition A:

If any of the recorded run-on times are longer than the one recorded for the rated balance condition, then the non-shaded parameter combinations also require testing.

For test condition B and C:

If run-on times are still increasing at the 95% or 105% points, additional 1% increments is taken until run-on times begin decreasing.